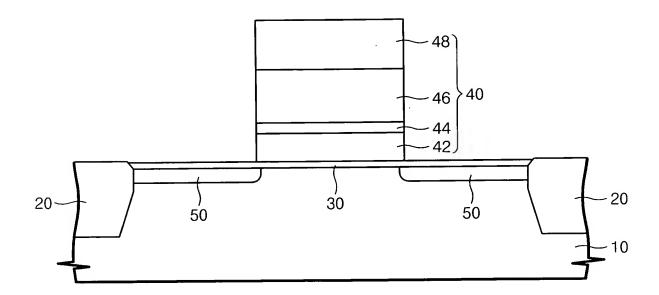
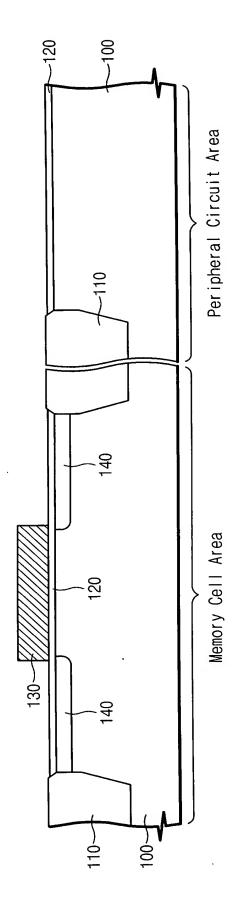
Fig. 1

(Prior Art)





 $\begin{array}{c}
-154 \\
-152 \\
-120
\end{array}$ -186 164~ 184~ 172 — 162 —

7100

140

120 130

140

150

Peripheral Circuit Area

Memory Cell Area

Fig. 3

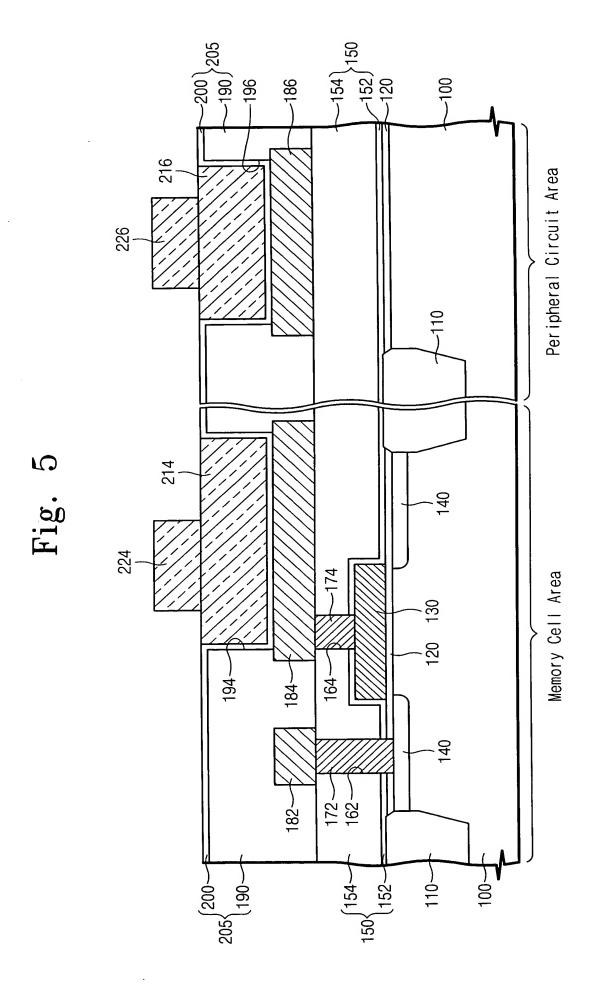
٠.

-200 -190 -196-154 -152 -120 -186 100 140 -174 194-164~ 184~ 140 172~ 162~ 1001 (154— (152— 205 (200—

Peripheral Circuit Area

Memory Cell Area

Fig. 4



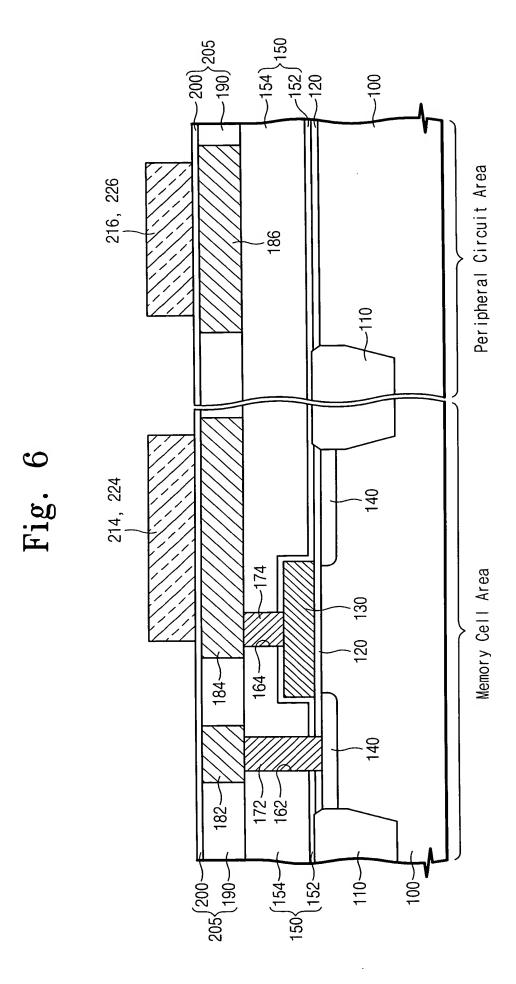


Fig. 7A

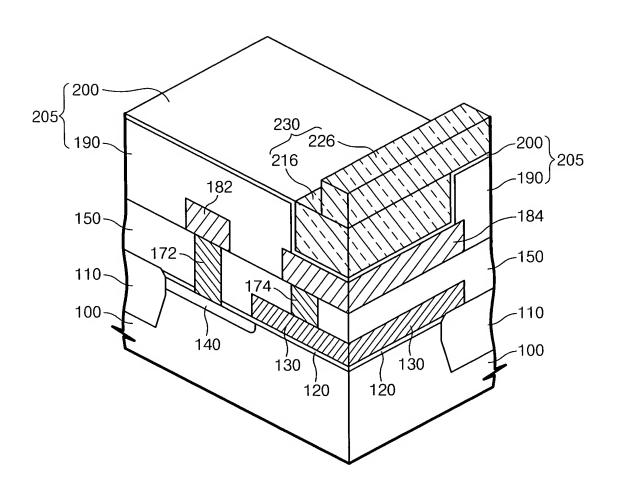


Fig. 7B

